

U.S. Patent Application Serial No. 09/531,677  
Amendment Under 37 C.F.R. §1.111 dated September 30, 2003  
Reply to the First Rejection of May 2, 2003

Amendments to the Specification:

In The Abstract:

Amend the current Abstract to read as follows:

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ABSTRACT OF THE DISCLOSURE

A power saving operation control method and a power saving operation control device for a phase comparator unit are provided. A power saving operation The control device circuit of the present invention includes: a reference signal frequency dividing unit which divides the frequency of a reference signal to generate a reference frequency divided signal; a comparison signal frequency unit which divides an input signal to generate a comparison frequency divided signal; a phase comparator which compares the phases of the reference frequency divided signal and the comparison frequency divided signal; a canceling signal generator which generates a power saving state canceling signal in accordance with the reference frequency divided signal and the comparison frequency divided signal; a first initializing signal generator which generates a first initializing signal for initializing the reference signal frequency dividing unit in accordance with the power saving state canceling signal; and a second initializing signal generator which generates a second initializing signal for initializing the comparison signal frequency dividing unit in accordance with the power saving state canceling signal.